

Notice of References Cited	Application/Control No. 10/075,783	Applicant(s)/Patent Under Reexamination GAUTHIER ET AL.	
	Examiner Fred Ferris	Art Unit 2128	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,370,678 B1	04-2002	Culler, Jason H	716/18
	B	US-6,446,016 B1	09-2002	Zhu, Qing	702/69
	C	US-2003/0154064 A1	08-2003	Gauthier et al.	703/14
	D	US-6,782,347 B2	08-2004	Hirano et al.	702/183
	E	US-6,704,680 B2	03-2004	Amick et al.	702/130
	F	US-6,727,737 B2	04-2004	Trivedi et al.	327/158
	G	US-6,664,831 B2	12-2003	Gauthier et al.	327/159
	H	US-6,687,881 B2	02-2004	Gauthier et al.	716/2
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Analysis fo Jitter due to Power-Supply Noise in Phase-Locked Loops, Heydari, IEEE Custom Integrated Circuits Conference, IEEE 2000
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.